



SDmagc

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Hideki Yoshikawa et al.

Group Art Unit: 2811

Serial No.: 10/073,117

Examiner: Junghwa M IM

Filed: February 12, 2002

P.T.O. Confirmation No.: 2329

For: COMPOSITE DEVICES OF LAMINATE TYPE AND PROCESSES

RESPONSE TO THE RESTRICTION REQUIREMENT

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Date: February 23, 2004

Sir:

This is in response to the Office Action of January 23, 2004, requiring restriction between alleged inventions under the provisions of 35 USC § 121.

In the Action, the examiner made a restriction requirement between the inventions of Group I drawn to a semiconductor device (claims 1-5) and Group II drawn to a method of fabricating a semiconductor device (claims 6-9).

Applicants hereby provisionally elect **Group I, that is, claims 1-5**, for examination on the merits in this application. Applicants reserve the right to file one or more divisional applications directed to the subject matter of the non-elected claims.

Favorable consideration of the subject application is respectfully requested.

Serial No.: 10/073,117
OA dated January 23, 2004
Response dated February 23, 2004

In the event this paper is not timely filed, the undersigned hereby petitions for an appropriate extension of time. The fee for this extension may be charged to Deposit Account No. 01-2340, along with any other additional fees which may be required with respect to this response.

Respectfully submitted,

ARMSTRONG, KRATZ, QUINTOS, HANSON & BROOKS, LLP



Donald W. Hanson
Attorney for Applicants
Reg. No. 27,133

Atty. Docket No. 020175
Suite 1000, 1725 K Street, N.W.
Washington, D.C. 20006
(202) 659-2930
DWH/nk



23850
PATENT TRADEMARK OFFICE